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Appl. No. to be assigned
July 30, 2003

AMENDMENTS TO THE SPECIFICATION:

Please amend the paragraph beginning at page 5, line 1, as follows:

~~Fig. 9 is a graph Figs. 9A-9E are graphs showing ON-OFF periodic arrivals.~~

Please amend the paragraph beginning at page 7, line 1, as follows:

Fig. 30A - Fig. 30B are graphs showing contours of q, B, and m for traffic cases 36, 38, 57, respectively.

Please amend the paragraph on page 9, beginning at line 6 as follows:

As mentioned above, the overall operation of ATM node 20 is managed by node main processor 40. In order to communicate with the node entities 30, and particularly with the entity processors 50 of the respective node entities 30, certain control paths must be established between node main processor 40 and the entity processors 50 so that the processors can communicate with one another. The communication is performed by cells which are transmitted over the control paths established between node main processor 40 and the various entity processors 50. Establishment of these control paths is understood with reference to U.S. Patent ~~6,480,492, Application SN 09/249,785 filed February 16, 1999, "Establishing Internal Control Paths in ATM Node,"~~ which is incorporated herein by reference.

Please amend the paragraph on page 10, beginning at line 3 as follows:

Examples of the components of a node entity 30 are described, for example, in the following United States Patent Applications (all of which are incorporated herein by reference): U.S. Patent Application SN 08/893,507 for "Augmentation of ATM Cell With Buffering Data"; U.S. Patent ~~6,128,295 Application SN 08/893,677 for "Buffering of Point-to-Point and/or Point-to-Multipoint ATM Cells"~~; U.S. Patent ~~6,034,958~~

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~~Application SN 08/893,479 for "VP/VC Look Up Function"; U.S. Provisional Application Serial No. 60/086,619 for "Asynchronous Transfer Mode Switch."~~ The structure and operation of node 20 is also further understood with reference to U.S. Patent 6,483,831, ~~Application SN 09/188,101, "Asynchronous Transfer Mode Switch,"~~ and U.S. Patent Application SN 09/188,265, "Asynchronous Transfer Mode Switch", both of which are incorporated herein by reference.

Please amend the paragraph on page 27, beginning at line 4 as follows:

Fig. 7 shows a simplified diagram of the queueing system in the spatial switch SPAS (core 24 in Fig. 31) in a wideband CDMA telecommunications system. As mentioned above, details of the spatial switch SPAS beyond those present here are discussed in U.S. Patent 6,483,831, ~~Application SN 09/188,101, "Asynchronous Transfer Mode Switch,"~~ and U.S. Patent Application SN 09/188,265, "Asynchronous Transfer Mode Switch", both of which are incorporated herein by reference.